

Author's Biography

Sandip Ghosh was born to Sri Nirapada Ghosh and Smt. Arati Ghosh on 6th January, 1978 at Bainchee, West Bengal, India. He obtained a Bachelor's degree in Electrical Engineering from Bengal Engineering College (D. U.), Howrah, West Bengal in 1999 and a Master in Control System Engineering from Jadavpur University, Kolkata, West Bengal in 2003. He joined the Department of Electrical Engineering, Indian Institute of Technology, Kharagpur in July 2003 as an Institute research scholar to pursue PhD. Since December, 2007 he is serving as a faculty member at the Department of Electrical Engineering, National Institute of Technology, Rourkela. His areas of academic pursuit include decentralized control, time-delay systems, robust control.

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