

# Resume



1.	Name in full (in capital letters) (Surname Underlined)	<b>YATENDRA KUMAR <u>SINGH</u></b>		
2.	Father's Name	<b>Shri SURENDRA NATH SINGH</b>		
3.	a. Marital Status	<b>Married</b>	b. Male / Female : <b>Male</b>	
4.	a. Permanent address	<b>C/o Shri Chandrika Prasad Singh, Village &amp; Post: Bhashampur, Sevapuri, Varanasi – 224 013, U.P.</b>		
	Phone No. (including STD code)	<b>+919932573239 (Cell)</b>	E-mail ID	<b>yatendra@ece.iitkgp.ernet.in yatendar_ctm@yahoo.com</b>
5.	Date of birth	<b>27-Jul-1979</b>		
6.	Citizenship	<b>INDIAN</b>		

7. Details of educational qualifications: Particulars of all examinations passed and degrees obtained commencing with the High School Leaving Examination (10<sup>th</sup> standard / Matriculation).

Sl. No.	School / College / Institute	Date of entry	Date of leaving	Name of the Board / University / Institution	Examination / Degree / Diploma passed	Distinction / Class / Division	Subjects (Please mention field of specialization, honours, etc, where applicable)	Percentage of marks or C.P.I.	Date of passing
1.	IIT Kharagpur	05-Jan-2005	18 Mar 2010 (submission)	IIT Kharagpur	PhD	N/A	RF & Microwave Engineering	N/A	30 June 2010
2.	IIT Kharagpur	Jan-2002	Mar-2004	IIT Kharagpur	MS	First	RF & Microwave Engineering	9.06/10	06-oct-2004
3.	IIT Kharagpur	Sept-1997	May-2001	IIT Kharagpur	BTech	First	Electronics & Electrical Communication Engineering	6.83/10	May-2001
4.	Kendriya Vidyalaya	June-1995	April-1996	CBSE	AISSE	First	Physics, Chemistry, Mathematics, English, Hindi.	70/100	July-1996
5.	Kendriya Vidyalaya	June-1993	April-1994	CBSE	AISSE	First	Mathematics, General-Science, Social Sciences, English, Hindi	71.8/100	July-1994

8. Details of employments: Particulars of present and past employments in chronological order, starting with the latest one:

Sl. No.	Organisation / Institute	Position held	Nature of duties / work	Date of joining	Date of leaving	Last Pay & Scale of pay
1.	IIT Kharagpur	Senior Project Officer	Research & Development	09-Aug-2008	June 2010	INR 25,000/- (consolidated)
2.	IIT Kharagpur	Senior Project Officer	Research & Development	09-Aug-2005	08-Aug-2008	INR 22,000/- (consolidated)
3.	IIT Kharagpur	Junior Project Officer	Research & Development	Feb 2002	08-Aug-2005	INR 12,875/- (consolidated)

9.	(a) Awards and honours:			
	<b>Sl. No.</b>	<b>Name of the Award and honours</b>	<b>Year</b>	<b>By</b>
	1.	Young Engineers Award	2009	Institution of Engineers (India)
	2.	Young Scientist Award	2008	International Union of Radio Science (URSI), Belgium
3.	Certificate of Merit (in Science)	1994	Central Board of Secondary Education (CBSE)	
10.	Additional Remarks:	<ol style="list-style-type: none"><li>1. Reviewer of paper for IEEE Transaction on Dielectrics and Electrical Insulation in 2010.</li><li>2. From August 2005 to July 2006, served as Governor of Yoga and Health Club run by gymkhana, IIT Kharagpur.</li></ol>		

I hereby declare that I have carefully read and understood the instructions and particulars supplied to me and that all the entries in this form are true to the best of my knowledge and belief.

Date: 02-July-2010

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Signature of the Candidate

## Sponsored Project/Consultancy Experiences

### Co-Principal Investigator of Sponsored Projects:

1. **Project Title** : Setting up of Dielectric Measurement Facility (Hardware & software)  
**Principal Investigator** : Prof. Amitabha Bhattacharya  
**Co-Principal Investigators** : Prof. A. Chakrabarty, Mr. Y.K. Singh  
**Sponsor** : Armament Research & Development Establishment, Pune, DRDO  
**Period** : Sept 2008 to March 2010
2. **Project Title** : Determination of Frequency Window for Communication with Buried Munitions  
**Principal Investigator** : Prof. Amitabha Bhattacharya  
**Co-Principal Investigators** : Prof. A. Chakrabarty, Mr. Y.K. Singh  
**Sponsor** : Armament Research & Development Establishment, Pune, DRDO  
**Period** : Sept 2008 to March 2010

## List of Publications

### Referred International Journals:

1. **Y. K. Singh**, and A. Chakrabarty, "Miniaturized Dual-Mode bandpass filter using stub-loaded Square Ring Resonator," *Microwave and Optical Technology Letters* vol. 51, no. 8, August 2009. pp. 1927-1929.
2. **Y. K. Singh**, and A. Chakrabarty, "Miniaturized Dual-Mode Circular Patch Bandpass Filters with Wide Harmonic Separation," *IEEE Microwave and Component Letters*, vol. 19, no. 9, Sept 2008, pp. 584-586.
3. Abdulla P, A. B. Kakade, **Y. K. Singh**, and A. Chakrabarty, "Analysis of Dielectric Resonator Antennas Excited by a Slot at the Waveguide Shorted End," *Microwave and Optical Technology Letters*, vol. 50, no. 5, May 2008, pp. 1356-1359.

### International Conferences/Symposiums:

1. **Y. K. Singh**, Abdulla P, and Ajay Chakrabarty, "Miniaturized Bandpass Filter using a Dual-Mode Octagonal Patch Resonator," *APMC 2009*, December 7-10, 2009, Singapore.
2. Abdulla P, **Y. K. Singh**, and Ajay Chakrabarty, "Theoretical and Experimental Study on Broad wall Slot Coupled Dielectric Resonator Antennas," *APMC 2009*, December 7-10, 2009, Singapore.
3. **Y. K. Singh**, Abdulla P, and Ajay Chakrabarty, "Defected Ground Structure Coupled Miniaturized Dual-Mode Square Patch Bandpass Filter," *URSI GA 2008*, August 7-16, 2008, Chicago, USA.
4. Abdulla P, **Y. K. Singh**, and Ajay Chakrabarty, "Coplanar Waveguide-Fed Slot Coupled Hemispherical Dielectric Resonator Antenna," *EUROEM 2008*, July 21-25, 2008, Swiss Federal Institute of Technology (EPFL), Lausanne, Switzerland.
5. **Y. K. Singh**, and A. Chakrabarty, "Design and Sensitivity analysis highly compact comparator for Ku-band monopulse radar," *International Radar Symposium – 2006*, May 23-26, 2006, Krakow, Poland.
6. S. Ghosh, **Y. K. Singh**, and A. Chakrabarty, "Estimation of Antenna Factor of Loaded Wire Antenna," *International Conference on Electromagnetic Interference & Compatibility 2006*, February 22-24, 2006, Bangalore, India.
7. **Y. K. Singh**, S. Ghosh, A. Chakrabarty, S. Sanyal, G. Sahoo, "Comparison of IE3D and FIDELITY Simulations for Planar Circuit Design," *International Conference On Electromagnetic Interference & Compatibility 2001/2002*, February 21-23, 2002, Bangalore, India.

### National Conferences/Symposiums:

1. **Y. K. Singh**, A. Chakrabarty, "Comparison of the IE3D and CST-Microwave Studio Simulators for Planar Microwave Filter Design," *Electro-IT BHU*, Feb 2005, India.
2. **Y. K. Singh**, K. Prathyush, S. Ranjan, S. Suthram, S. Ghosh, A. Chakrabarty, S. Sanyal, "Design of a Microstrip Patch Antenna Array Using IE3D Software," *National Conference On Communication*, Jan31-Feb 2, 2003, IIT Madras, India.