Pradyut Kumar Biswal obtained his AMIE and M.Tech from Institution of Engineer's (India) and Visvesvaraya National Institute of Technology, Nagpur, India in 2000 and 2002 respectively.   He joined the Ph.D. program in the Department of E&ECE, IIT Kharagpur as an institute scholar in 2007. He successfully completed his Ph.D. degree in 2012.

Prior to joining Ph.D., he was working as a Senior Lecturer in Electronics Department at Silicon Institute of Technology, Bhubaneswar, Odisha. Currently, he is positioned as an Assistant Professor in International Institute of Information Technology (IIIT), Bhubaneswar, India. He teaches Microprocessor, Digital Signal Processing, VLSI Design. He has a teaching experience of 5 years. His research interest includes Digital Image processing, Architecture design for image processing algorithms and FPGA implementations.

Mr. Biswal is a Member of IEEE, USA and associate member of IEI (India). He has published 2 papers in international journals and 5 conference papers.