An Optimization-based Methodology for High-Level Design of Analog Systems



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An Optimization-based Methodology for High-Level Design of Analog Systems

Thesis submitted to the
Indian Institute of Technology, Kharagpur
For award of the degree

of

Doctor of Philosophy (Ph.D.)

by

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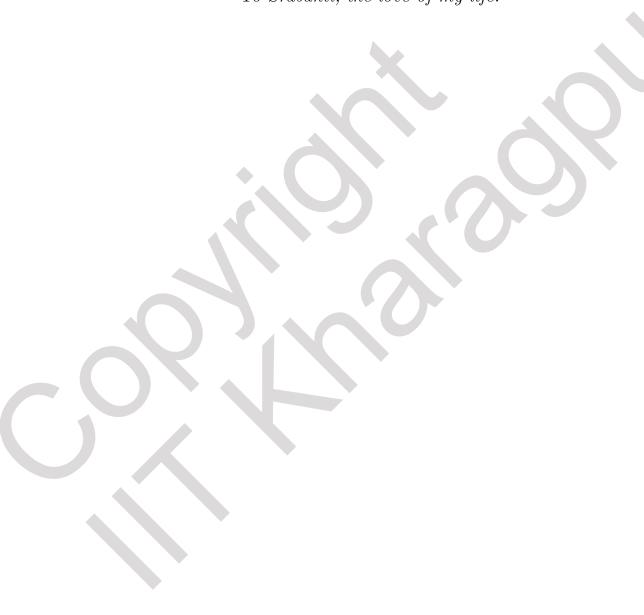
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To my parents for their constant support throughout my life.

To Srabanti, the love of my life.





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