

An Optimization-based Methodology for High-Level Design of Analog Systems

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An Optimization-based Methodology for High-Level Design of Analog Systems

*Thesis submitted to the
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For award of the degree
of*

Doctor of Philosophy (Ph.D.)

by

Soumya Pandit



**School of Information Technology
Indian Institute of Technology, Kharagpur**

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To my parents for their constant support throughout my life.

To Srabanti, the love of my life.

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