Arijit Mondal received the B.Tech (Hons) degree in Electrical Engineering and his M.S. degree in Computer Science & Engg. in 2001 and 2005 respectively, both from the Indian Institute of Technology (IIT), Kharagpur, India. His current research interests include timing analysis, VLSI CAD and analog simulation. Arijit may be contacted at arijit@cse.iitkgp.ernet.in