

Curriculum Vitae

Rupsa Chakraborty, daughter of Somen Chakraborty and Minati Chakraborty, was born on June 5, 1980 in Kolkata, India.

After passing the Indian School Certificate Examination (ISCE) in the year 1999 from ST. Thomas' Girls' School Kidderpore, Kolkata, she joined Siliguri Institute of Technology under the University of North Bengal to pursue the study of Bachelor of Engineering (B.E.) in Information Technology. After graduating in the year 2003, she pursued further study of Master of Engineering (M.E.) from the Department of Computer Science and Technology at the Bengal Engineering and Science University Shibpur, with GATE Scholarship. After completion of the Master's course in the year 2005, she joined the Department of Computer Science and Engineering at the Indian Institute of Technology Kharagpur in the same year, as a PhD student. During this time, her research area has been broadly in the field of VLSI testing and timing verification. Research work presented in this thesis has been the outcome of that effort. Her other research interests include image processing and computer graphics.

Disseminations

A list of publications by the author of this thesis, which are directly related to this thesis, are given below:

1. R. Chakraborty, D. Roy Chowdhury, "A Hierarchical Approach Towards System Level Static Timing Verification of SoCs", *accepted for publication in Proc. of 27th IEEE International Conference on Computer Design (ICCD 2009)*, California, October 2009
2. R. Chakraborty, D. Roy Chowdhury, "A Novel Seed Selection Algorithm for Test Time Reduction in BIST", *accepted for publication in Proc. of Asian Test Symposium (ATS'09)*, Taiwan, November 2009
3. R. Chakraborty, D. Roy Chowdhury, "A Centralized BIST Infrastructure Design For Stuck-At Fault Detection in SoC", *IEEE/VLSI Design and Test Symposium, VDAT'09*, Bangalore, July 2009
4. R. Chakraborty, D. Roy Chowdhury, "coreBIST: A Cellular Automata Based Core for Self Testing System-on-Chips", *Proc. LNCS Springer, 8th Intl. Conf. on Cellular Automata for Research and Industry, ACRI'08*, pp. 506-511, Japan, Sept. 2008
5. R. Chakraborty, D. Roy Chowdhury, "Design of a BIST-Core Using Cellular Automata for IP-core and Interconnect Testing", *4th International Workshop on Reconfigurable Communication Centric System-on-Chips RecoSoC'08*, pp. 1-8, Barcelona, July 2008.
6. R. Chakraborty, D. Roy Chowdhury, "Raising the Level of Abstraction for the Timing Verification of System-on-Chip", *Proc. IEEE Computer Society Annual Symposium on VLSI, ISVLSI'08*, pp. 459-462, France, April 2008

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7. R. Chakraborty, D. Roy Chowdhury, "Scheduling of Cores for Power Constrained System-on-Chip Testing", *15th International Conference on Advanced Computing & Communication, ADCOM'07*, pp. 9-14, Guwahati, December 2007

Other publications of the author of this thesis include:

1. R. Chakraborty, J. Sil, "Handwritten Character Recognition Systems Using Image-Fusion and Fuzzy Logic", *LNCS Springer, Proc. International Conference on Pattern Recognition and Machine Intelligence, PreMI'05*, pp. 344-349, Dec. 2005
2. R. Chakraborty, J. Sil, "Design of an intelligent system for the recognition of handwritten characters", *Proc. of International Conference of Cognition and Recognition ICCR'05*, pp. 598-602, 2005
3. R. Chakraborty, J. Sil, "Development of an Intelligent System for Extraction and Recognition of Handwritten Characters", *Journal of Computer Sciences, The Icfai University Press*, Vol. 1, No. 2, pp. 7-30, October 2007