

NEW APPROACHES FOR SIMULATION  
BASED VERIFICATION OF PIPELINED  
PROCESSORS

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# New Approaches for Simulation Based Verification of Pipelined Processors

*Thesis submitted to  
Indian Institute of Technology  
for the award of the degree  
of*

Doctor of Philosophy

*by*

**Subrat Kumar Panda**

*under the guidance of*

**Prof. Partha Pratim Chakrabarti**

and

**Prof. Rajeev Kumar**



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This is to certify that the thesis entitled **New Approaches for Simulation Based Verification of Pipelined Processors**, submitted by **Subrat Kumar Panda** to Indian Institute of Technology, Kharagpur, is a record of bona fide research work under our supervision and is worthy of consideration for the award of the degree of Doctor of Philosophy of the Institute.

Prof. Partha Pratim Chakrabarti  
Supervisor

Prof. Rajeev Kumar  
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