

Curriculum Vitae

SANTOSH GHOSH, son of Mr. Sadhan Ghosh and Mrs. Ambika Ghosh, was born on September 10, 1978 at Kshiragram, Burdwan, WB, India. After passing the Higher Secondary (10+2) Examination from Burdwan Municipal High School, Burdwan, he joined Haldia Institute of Technology (HIT) under the Vidyasagar University to pursue the study of Bachelor of Technology (B.Tech.) in Computer Science and Engineering (CSE) and subsequently joined as a lecturer at CSE department of HIT in 2002. He pursued for further study of Master of Science (M.S.) from the Department of Computer Science and Engineering at the Indian Institute of Technology Kharagpur in 2006. After completion of the Master's course in the year 2008, he joined the same department in the same year, as a PhD student. During this time, his research area has been broadly in the field of Cryptographic Hardware and Side-channel Attacks. Research work presented in this thesis has been the outcome of that effort. His other research interests include VLSI Design and Testing.

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